

JCS55 U.S. PTO
10/028276
12/28/01

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

| APPL NUM | FILING DATE | CLASS | SUBCLASS | GAU | EXAMINER |
|----------|-------------|-------|----------|------|----------|
| 10028276 | 12/28/2001 | 345 | 92 | 2673 | Piziali |

**APPLICANTS: Yamazaki Shunpei; Koyama Jun;

**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A DIV OF 09/144,538 08/31/1998 PAT 6,335,716 - *es*

** FOREIGN APPLICATIONS VERIFIED:

JAPAN 9-254257 09/03/1997 - *es*

JAPAN 10-156696 05/20/1998 - *es*

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed
35 USC 119 conditions met

☒ yes ☐ no
☒ yes ☐ no

ATTORNEY DOCKET NO

740756-2414

Verified and Acknowledged: J Examiners's initials *es*

TITLE : Semiconductor display device correcting system and correcting method of semiconductor display device

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED

Assistant Examiner

CLAIMS ALLOWED

Total Claims

Print Claim for
O.G.

ISSUE FEE

Amount Due

Date Paid

DRAWING

Sheets Drwg.

Figs. Drwg.

Print Fig.

Primary Examiner

PREPARED FOR ISSUE

Application Examiner



TERMINAL

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☐ CD-ROM

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